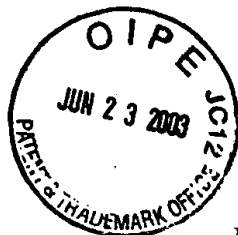


G O D W I N G R U B E R

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Commissioner for Patents
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Washington, D.C. 20231

Re: Patent Application for:
"Additional n-Type LDD/Pocket Implant for Improving Short Channel
NMOS ESD Robustness"
Serial No.: 09/876,292
Attorney Docket: TI-31089
Our File: 50000.2138

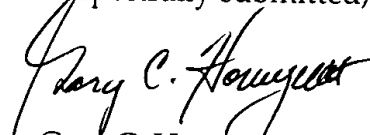
Dear Sir:

Enclosed for filing please find the following items relating to the above-identified application:

- (1) Amendment Pursuant to 37 CFR § 1.115; and
- (2) Postcards.

Please return the date-stamped postcards to the corresponding addresses as indicated. In the meantime, if you have any questions or comments concerning this matter, please call the undersigned. Otherwise, please accept the enclosed.

Respectfully submitted,


Gary C. Honeycutt

GCH:glc
Enclosures

cc: W. James Brady, III (Texas Instruments Incorporated, w/ encls.)

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